

revision 0.1

Solid State Drive

MSD-PATA3018

Product Specification

Jun. 19th, 2008



Revision History				
No.	Date	History	Reference	Remark
0.1	Jun. 19. 2008	Initial issue		Preliminary

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1. General Description

1.1 Overview

Mtron SSD is a high performance storage device designed to solve the bottleneck of computing system by traditional hard disk drives. Mtron SSD can be drop-in replaced with the hard disk drives without any additional software installation. Customer can benefit instantly from Mtron SSD's high random access performance and solve the legacy problem of access bottleneck by traditional hard disk drives.

Mtron SSD purely consists of semiconductor devices and NAND flash memories, which give rugged features against shock and vibration with an increased MTBF. Further more, Mtron SSD has highly elaborated and advanced flash memory management algorithm to guarantee high data reliability.

With Mtron SSD's superior performance, reliability and durability, Mtron SSD increases overall performance and values of system in mobile computing and enterprise system solutions.

1.2 Feature

- > Performance
 - Maximum sustained read: 100MB/s
 - Maximum sustained write: 100MB/s
 - Maximum IOPS: 48,000 (sequential write by 512B unit)
- > Form factor: 71.0mm x 54.0mm x 8.0mm
- > Memory type: SLC NAND flash memory
- > Interface standard: ATA-7
- > Transfer mode: PIO mode (0~4), DMA mode (0~2), UDMA mode (0~6)
- > Connector type: ZIF (Zero Insertion Force) type
- > Storage capacity: 16GB, 32GB
- > Input voltage: 3.3VCC ($\pm 5\%$)
- > Flash management algorithm: static and dynamic wear-leveling, bad block management algorithm
- > ECC (Error Correction Code): BCH 7 bits ECC
- > Write endurance: >140 years @ 50GB write/day at 32GB SSD
- > Read endurance: unlimited
- > Data retention: 10 years

1.3 Block Diagram

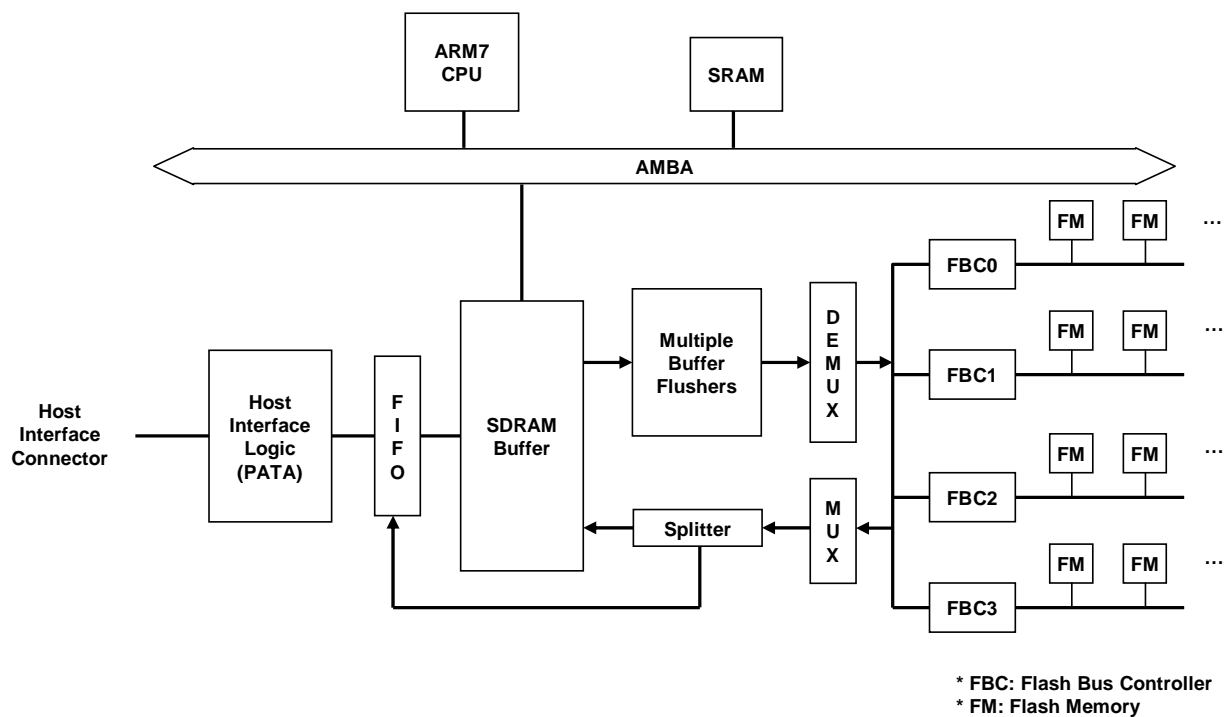


Figure 1 Block Diagram

2. Physical Specifications

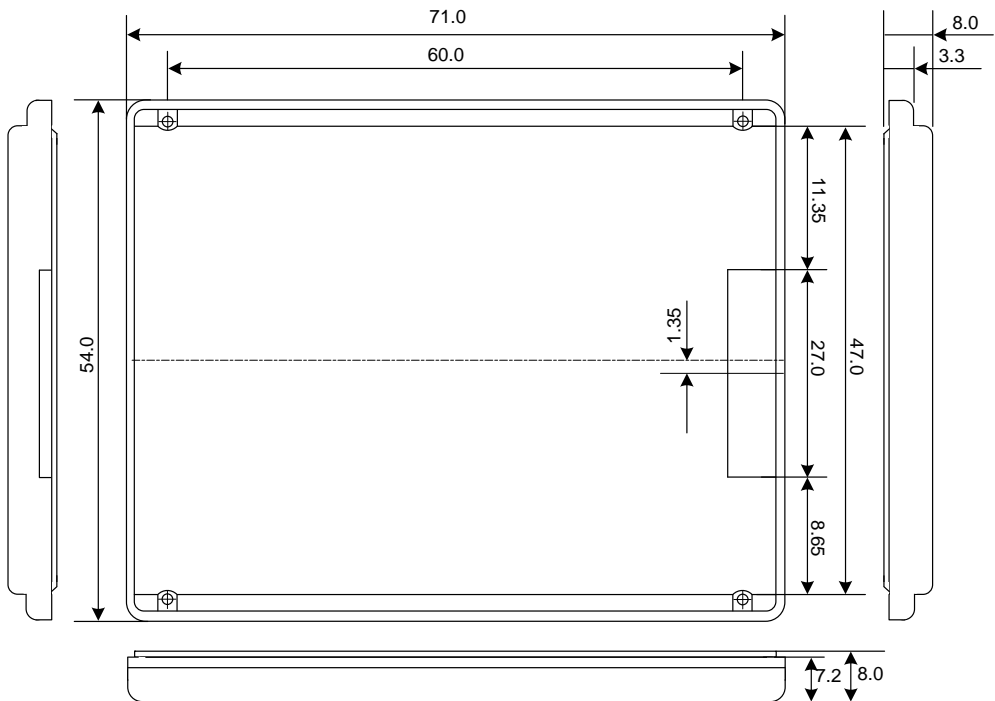
2.1 Physical Dimensions

Table 1 Physical Dimensions and Weight

Parameter	Value	Unit
Height	8.0 ± 0.25	mm
Width	54.0 ± 0.20	mm
Length	71.0 ± 0.25	mm
Maximum Weight	53	g

Note:

1. Case material: Aluminum
2. Test drive: MSD-PATA3018-032



(Unit: mm)

Figure 2 Physical Dimensions

3. Product Specifications

3.1 Interface

- > The interface of MSD-PATA 3018 complies with the ATA-7 standard
- > Transfer modes:
 - PIO mode 0, 1, 2, 3, 4
 - DMA mode 0, 1, 2
 - UDMA mode 0, 1, 2, 3, 4, 5, 6

3.2 Capacity

Table 2 Capacity Specifications

Capacity	16GB	32GB
Total Byte	16,001,269,760	32,002,539,520
Cylinders	16383*	16383*
Heads	16	16
Sectors	63	63
Max. LBA	31,252,480	62,504,960

Note:

1. 16383: Cylinders, Heads, Sectors are not applicable for the capacities. Only LBA addressing applies.

3.3 Performance

3.3.1 Transfer rate and IOPS

Table 3 Maximum Data Transfer Rate

Data Transfer	Maximum Read	Maximum Write	Unit
Sustained Rate	100	100	MB/s

Note:

1. Test PC: AMD Athlon™ X2 4800+ DDR2 PC6400 1GB*2pcs (Dual Channel), NVIDIA nForce 570 Ultra MCP (UDMA133), Windows XP SP2
2. Benchmarking program: ATTO Disk benchmark 2.41 (Transfer unit size = 128KB)
3. Test drive: MSD-PATA3018-032

Table 4 IO Performance Values

Access Type	IOPS operation unit size	
	512B	4KB
Sequential read	25,000	13,000
Sequential write	48,000	20,000
Random read	16,000	10,000
Random write	60	60

Note:

1. Test PC: AMD Athlon™ X2 4800+ DDR2 PC6400 1GB*2pcs (Dual Channel), NVIDIA nForce 570 Ultra MCP (UDMA133), Windows XP SP2
2. Benchmarking program: IOMeter 2006.07.27 (Maximum disk size = 2,000,000 sectors (1GB), Outstanding I/O per target = 8, Duration = 1min)
3. Test drive: MSD-PATA3018-032

3.3.2 Access time

Random access time: 0.1 msec.

Note:

1. Test PC: AMD Athlon™ X2 4800+ DDR2 PC6400 1GB*2pcs (Dual Channel), NVIDIA nForce 570 Ultra MCP (UDMA133), Windows XP SP2
2. Benchmarking Program: HDTach 3.0.1
3. Test drive: MSD-PATA3018-032

3.4 Reliability Characteristics

3.4.1 Wear-leveling

Mtron SSD supports both static and dynamic wear-leveling. These two algorithms guarantee the use of all flash memory at the same level of write/erase cycles to improve lifetime limitation of NAND based storage.

3.4.2 Endurance

Table 5 Endurance

Write	At 32GB capacity: >140 years @ 50GB write/day
Read	Unlimited

Note:

1. The above calculation is based on the guaranteed 100,000 program and erase cycles of flash memory from vendors and the assumption that the write is performed in sequential manner.

3.4.3 ECC

BCH ECC (Error Correction Code) corrects random 7 bits error in 512B data.

3.4.4 Bad block management algorithm

This algorithm replaces bad blocks with new ones from available spares on media error conditions.

3.4.5 MTBF

TBD

3.4.6 Data retention

Data retention at 25°C: >10 years

4. Electrical Specifications

4.1 ZIF Connector Description

4.1.1 Interface connector dimensions

The following diagram identifies the connector of MSD-PATA3018.

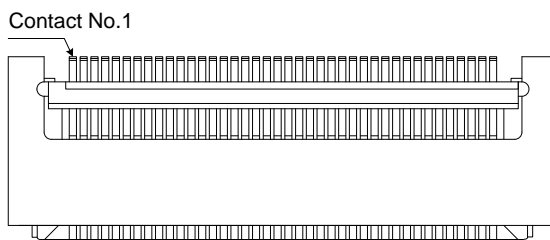


Figure 3 Connector Top Side

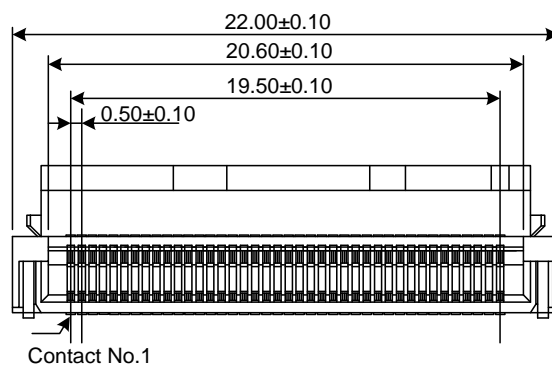


Figure 4 Connector Front Side

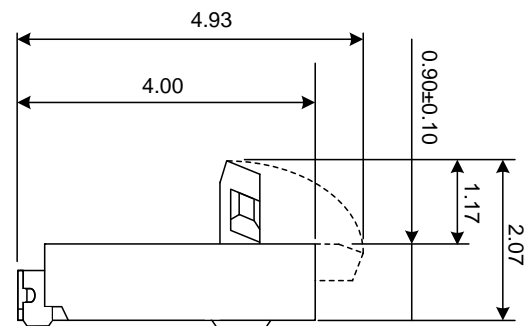


Figure 5 Connector Tip Side

Note:

1. Drive connector: DDK, FF14-40A-R11B
2. Lock lever should be opened before inserting FPC into the connector.

4.1.2 Pin assignment

Table 6 Pin Assignment

Pin Number	Signal Name	Pin Number	Signal Name
1	Reserved	2	Reserved
3	$\overline{\text{RESET}}$	4	GND
5	DD7	6	DD8
7	DD6	8	DD9
9	DD5	10	DD10
11	DD4	12	DD11
13	DD3	14	DD12
15	DD2	16	DD13
17	DD1	18	DD14
19	DD0	20	DD15
21	GND	22	DMARQ
23	GND	24	$\overline{\text{DIOW:STOP}}$
25	$\overline{\text{DIOR:HDMARDY:HSTROBE}}$	26	GND
27	$\overline{\text{IORDY:DDMARDY:DSTROBE}}$	28	GND
29	$\overline{\text{DMACK}}$	30	INTRQ
31	DA1	32	$\overline{\text{DIAG:CBLID}}$
33	DA0	34	DA2
35	$\overline{\text{CS0}}$	36	$\overline{\text{CS1}}$
37	$\overline{\text{DASP}}$	38	+3.3V (Logic)
39	+3.3V (Logic)	40	Reserved

4.2 FPC Description

4.2.1 FPC dimensions

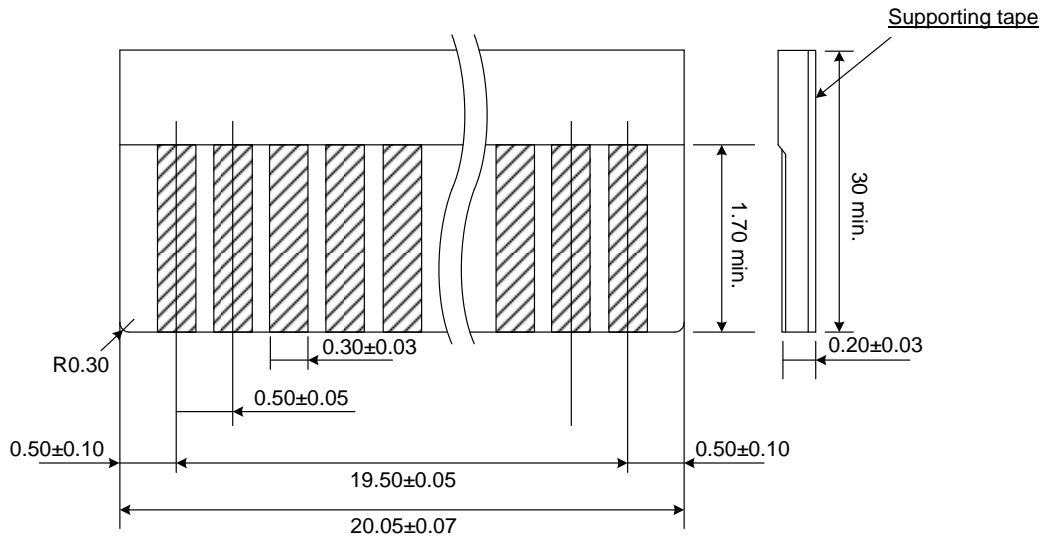


Figure 6 FPC* Dimensions

Note:
1. FPC: Flexible Printed Circuit

4.2.2 Recommended host side FPC

Table 7 Recommended Host Side FPC

Parameter	Specifications	Unit
Width	20.05 ± 0.07	mm
Thickness	0.20 ± 0.03	mm
Contact Resistance	Typical 50	ohm
Recommended Plating	Gold over Nickel plating	-
Durability	20 Times with same connector and FPC	-

4.3 Supply Voltage

Table 8 Supply Voltage

Parameter	Specifications	Unit
Main Supply Voltage	3.3 ± 5%	VDC
Maximum Ripple	70mV (peak to peak), 0-30MHz	-
Supply Rise Time	0.1 to 100	msec

4.4 Power Consumption

Table 9 Power Consumption

Parameter	Value	Unit
Idle	0.71	W
Sustained Read	1.00	W
Random Read	1.00	W
Sustained Write	2.58	W
Random Write	2.58	W

Note:

1. Measurement device: Agilent Technologies N6705A
2. Benchmarking program: IOMeter 2006.07.27 (Maximum disk size = 2,000,000 sectors (1GB), Outstanding I/O per target = 8, Duration = 1min, Transfer unit size = 256KB)
3. Supply voltage: 3.3V±5%
4. Test drive: MSD-PATA3018-032

5. Environmental Specifications

5.1 Temperature

Table 10 Temperature

Parameter	Specifications	Unit
Operating	0 to 70	°C
Non-Operating	-55 to 95	°C

Note:

1. Test duration: 500h
2. Test drive: MSD-PATA3018-032

5.2 Shock

TBD

5.3 Vibration

TBD

5.4 Humidity

Table 11 Operating Humidity

Parameter	Specifications	Unit
Operating	5 to 95	%

6. Command Description

6.1 Supporting ATA Command Set

Table 12 Supporting ATA Commands

Command Name	Command Code
Recalibrate	10h
Read Sector(s)	20h
Write Sector(s)	30h
Read Verify Sector(s)	40h
Execute Device Diagnostic	90h
Initialize Device Parameters	91h
Download Microcode	92h
SMART	B0h
Read Multiple	C4h
Write Multiple	C5h
Set Multiple Mode	C6h
Read DMA	C8h
Write DMA	CAh
Standby Immediate	E0h
Idle Immediate	E1h
Standby	E2h
Idle	E3h
Check Power Mode	E5h

Command Name	Command Code
Sleep	E6h
Flush Cache	E7h
Identify Device	ECh
Set Features	EFh
Read Native Max Address	F8h
Set Max Address	F9h

Note:

1. Firmware version: 0.18 R1

6.2 SMART Feature Set

6.2.1 Sub command set

Table 13 SMART Sub Command Set

SMART	
Read Data	D0h
Read Attribute Thresholds	D1h
Enable/Disable Autosave	D2h
Enable Operations	D8h
Disable Operations	D9h
Return Status	DAh

6.2.2 SMART data structure

Table 14 SMART Data Structure (READ DATA (D0h))

Byte	F/V	Description
0~1	X	Structure version (0x0001)
2	X	Attribute ID 0xBB: Total Erase Count *1
3~4	X	Flags (0x02) 15-6 Reserved 5-2 Vendor-specific 1 1=The value of this attribute is updated during both normal operation and offline activities 0 0=The usage of age of the device has exceeded its intended design life period
5	V	Total erase count current value *2
6	V	Total erase count worst value *3
7~12	V	Total erase count raw value *4
13~361	X	Vendor specific
362	V	Off-line data collection status
363	X	Self-test execution status byte
364~365	V	Total time in seconds to complete off-line data collection activity
366	X	Vendor specific
367	F	Off-line data collection capability
368~369	F	SMART capability (0x0002) 15-2 Reserved 1 1= Device supports the SMART ENABLE/DISABLE ATTRIBUTE AUTOSAVE command 0 1= Device saves SMART data prior to going into a power saving mode (Idle, Standby, or Sleep) or immediately upon return to Active or Idle mode from a Standby mode
370	F	Error logging capability (0x00) 7-1 Reserved 0 1=Device error logging supported
371	X	Vendor specific
372	F	Short self-test routine recommended polling time (in minutes)

Byte	F/V	Description
373	F	Extended self-test routine recommended polling time (in minutes)
374~385	R	Reserved
386~510	X	Vendor specific
511	V	Data structure checksum

Key :

F=the content of the byte is fixed and does not change.

V=the content of the byte is variable and may change depending on the state of the device or the commands executed by the device.

X=the content of the byte is vendor specific and may be fixed or variable.

R=the content of the byte is reserved and shall be zero.

Note:

1. Refer to 6.2.3
2. Refer to 6.2.3.1
3. Refer to 6.2.3.2
4. Refer to 6.2.3.3

Table 15 SMART Data Structure (READ Threshold (D1h))

Byte	F/V	Description
0~1	X	Structure version (0x0001)
2	X	Attribute ID 0xBB: Total Erase Count
3	X	Total erase count Threshold (0x00)
4~13	R	Reserved
14~361	R	2 nd ~ 30 th Attribute Threshold (reserved)
362~379	R	Reserved
380~510	X	Vendor specific
511	V	Data structure checksum

Key :

F=the content of the byte is fixed and does not change.

V=the content of the byte is variable and may change depending on the state of the device or the commands executed by the device.

X=the content of the byte is vendor specific and may be fixed or variable.

R=the content of the byte is reserved and shall be zero.

6.2.3 Total erase count

Total Erase Count means the summation of erase counts of all blocks in SSD.

In general, the maximum value of 32GB SSD's Total Erase Count is 3200M and if the Total Erase Count exceeds the value, it says that the SSD is completely wore out.

Maximum Total Erase Count differs from vendors of NAND FLASH Chips and the capacity of SSD.

6.2.3.1 Total erase count current value

This value becomes 100 when Total Erase Count is equal to 0 and becomes 0 when Total Erase Count is equal to the maximum value.

If the value is equal to 0, it means that the SSD is completely wore out.

6.2.3.2 Total erase count worst value

This value becomes 100 when Total Erase Count is equal to 0 and becomes 0 when Total Erase Count is equal to 90% of maximum value.

Users should be warned when this value becomes 0.

6.2.3.3 Total erase count raw value

This value means the summation of erase counts of all blocks in SSD.

6.3 Set Features

6.3.1 Set features register value

Table 16 Set Features Register Value

Set Features	
Enable Write Cache	02h
Set Transfer Mode	03h
Disable Read Look-ahead Feature	55h
Disable Reverting to Power-on Defaults	66h
Enable Read Look-ahead Feature	AAh
Enable Reverting to Power-on Defaults	CCh
Disable Write Cache	82h

7. Ordering Information

Table 17 Information

MSD-IFIF30FF-CCC		
MSD	Product Code	MSD: Mtron SSD MOBI
IFIF	Interface	PATA: PATA interface
30	Appliance	Mobile Platform (SLC)
FF	Form Factor	18: 1.8-inch
CCC	Unformatted Capacity (GB)	016: 16GB 032: 32GB

8. Contacts

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